

## LS XBM/XBC Cnet

Supported Series: LS XGB Series XBM/XBC CPU with communication module  
XGL-CH2A

Website: <http://www.lgjs.com/>

### HMI Setting:

Parameters	Recommended	Options	Notes
<b>PLC type</b>	LS XBM/XBC Cnet		
<b>PLC I/F</b>	RS232	RS232/RS485	
<b>Baud rate</b>	115200	9600~115200	
<b>Data bits</b>	8	7, 8	
<b>Parity</b>	None	Even, Odd, None	
<b>Stop bits</b>	1	1	
<b>PLC sta. no.</b>	1	0-31	

### Device Address:

Bit/Word	Device type	Format	Range	Memo
B	PW_Bit	DDDDh	0 ~ 2047f	I/O device Bit
B	MW_Bit	DDDDh	0 ~ 2047f	Internal device Bit
B	LW_Bit	DDDDDh	0 ~ 11263f	Communication device Bit
B	KW_Bit	DDDDh	0 ~ 4095f	Preservation device Bit
B	FW_Bit	DDDDh	0 ~ 2047f	Special device Bit( write available from 1025)
B	DW_Bit	DDDDDh	0 ~ 32767f	Data register_Bit expression (D0000.0)
B	UW_Bit	DH.DDh	0 ~ 7f.31f	XGK-CPUE : hh(0~1f)
B	RW_Bit	DDDDDh	0 ~ 32767f	
B	SX	DDDDD	0 ~ 12799	Relay for step control Bit
B	TX	DDDD	0 ~ 2047	Timer device Bit
B	CX	DDDD	0 ~ 2047	Counter device Bit
W	PW	DDDD	0 ~ 2047	I/O device_2,048 points
W	MW	DDDD	0 ~ 2047	Internal device_4,096 points
W	LW	DDDDD	0 ~ 11263	Communication device_20,480 points

Bit/Word	Device type	Format	Range	Memo
W	KW	DDDD	0 ~ 4095	Preservation device_4,096 points
W	FW	DDDD	0 ~ 2047	Special device_4,096 point
W	DW	DDDDD	0 ~ 32767	Data register_5120 words
W	UW	DH.DD	0.00 ~ 7f.31	Analog data register_256 words
W	RW	DDDDD	0 ~ 32767	
W	SW	DDDDD	0 ~ 127	Relay for step control
W	TW	DDDD	0 ~ 2047	Timer current value register_256 words
W	CW	DDDD	0 ~ 2047	Counter current value register_256 words
W	NW	DDDDD	0 ~ 21503	Communication data register_3,936 words
W	ZW	DDD	0 ~ 127	Index register_128 words

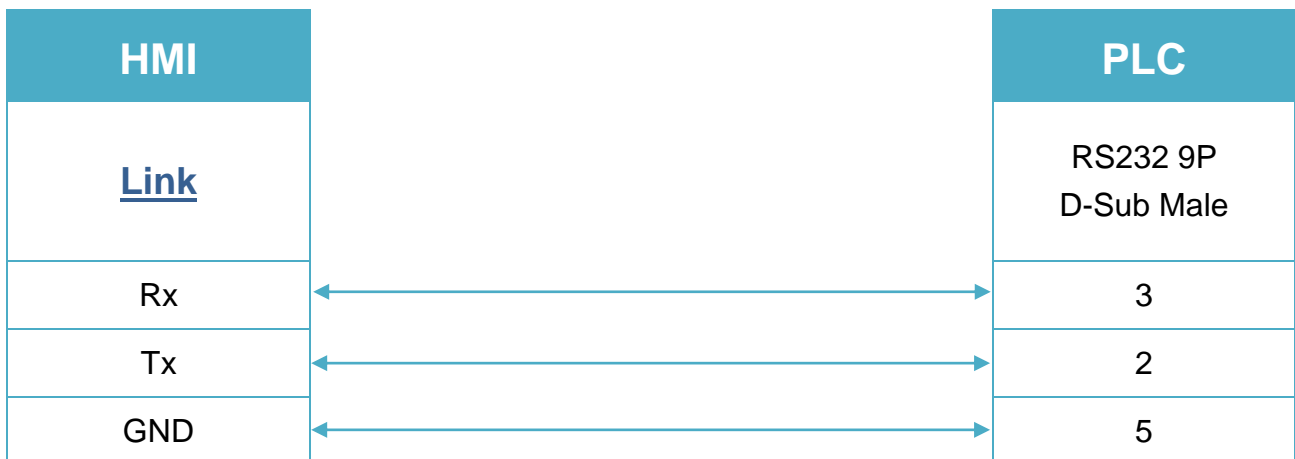
## Wiring Diagram:

RS232 9P D-Sub

### Diagram 1

#### RS-232

The serial port pin assignments may vary between HMI models, please click the following link for more information.



## Diagram 2

### RS-485 2W

The serial port pin assignments may vary between HMI models, please click the following link for more information.

